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Drafts
Pending
Active
L1: (0) (stand adj by) same (isolat$3 near3 (device circuit signal apparat
L2: (0) (stand adj by) adj signal
L3: (0) (stand adj by) near2 signal
L4: (3703) ((stand adj by) standby) near2 signal
L5: (415) ((stand adj by) standby) same (isolat$3 near3 (device circuit si
L6: (219) ((stand adj by) standby) with (isolat$3 near3 (device circuit si
L7: (80) ((stand adj by) standby) with (isolat$3 near3 signal)
L8: (33) ((stand adj by) standby) same (isolat$3 near3 signal) same (isola
L9: (35) ((stand adj by) standby) same (isolat$3 near3 signal) same (isola
Failed
Saved
(3059) ((processor CPU microprocessor) and ((power adj2 supply) with bus) a
(682) (((processor CPU microprocessor) and ((power adj2 supply) with bus)
(14) (((processor CPU microprocessor) and ((power adj2 supply) with bus)
(0) ((processor CPU microprocessor) and ((power adj2 supply) with bus) sam
(3) (((processor CPU microprocessor) and ((power adj2 supply) with bus)
(12) (((processor CPU microprocessor) and ((power adj2 supply) with bus)
(7) (((processor CPU microprocessor) and ((power adj2 supply) with bus)
(3) (((((processor CPU microprocessor) and ((power adj2 supply) with bus)
(532614) (input output) adj2 pad buffer
(136) ((input output) adj2 pad buffer) and ((internal near3 bus) with (pow
(122) (((input output) adj2 pad buffer) and ((internal near3 bus) with (po
(71) (((input output) adj2 pad buffer) and ((internal near3 bus) with (po
(2477) ((processor CPU microprocessor) and ((power adj2 supply) with bus)
(17) (((input output) adj2 pad buffer) and ((internal near3 bus) with (p
(152) (power adj supply adj bus) and (isolat$3 with (power adj supply))

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DB: USPAT,US-PGPG,EP,JP,GERVENT,BM,109

Default operator: OR

☐ Check

☒ Highlight all hit terms only

((stand adj by) standby) same (isolat\$3 near3 signal) same (isolat\$3
 near3 circuit device)

[illegible]